

ABSTRACT

The invention relates to a method and arrangement for decoding a turbo coded code word comprising termination bits. The received code word samples are applied to a decoder in a sequence required by the code structure. Termination samples are grouped according to different components of the code word, code interleavers are extended such that the extension part comprises addresses of the systematic termination samples. The address of the ascending order is formed after the actual code word samples such that the addresses of the extension part are addresses of the systematic termination samples corresponding to the termination samples of one or more parities of ascending order and those of the extrinsic weight coefficients related to said systematic termination samples. Decoding is executed by using the extended, ascending address formation and one or more extended interleavers.

(Figure 3)